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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/837,307	04/17/2001	Billy Garrett JR.	RBS2.P031	9703

30554 7590 01/17/2008
SHEMWELL MAHAMED I LLP
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SAN JOSE, CA 95129

EXAMINER

DINH, NGOC V

ART UNIT	PAPER NUMBER
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2189

MAIL DATE	DELIVERY MODE
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01/17/2008

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

09/837,307

Applicant(s)

GARRETT, BILLY

Examiner

NGOC V. DINH

Art Unit

2189

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 03 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 02/28/2007.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-30, 32-40, 43-55 and 62-91 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-30, 32-40, 43-55, 62-77, 89-91 is/are allowed.
- 6) ☒ Claim(s) 78, 82 and 85 is/are rejected.
- 7) ☒ Claim(s) 79-81, 83-84 and 86-88 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 17 April 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- ☒ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☒ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____
- ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- ☐ Notice of Informal Patent Application
- ☐ Other: _____

Continuation of Attachment(s) 3). Information Disclosure Statement(s) (PTO/SB/08), Paper No(s)/Mail Date
:07/27/05,01/23/06,04/10/06,01/25/07,.

DETAILED ACTION

1. This Office Action is responsive to U.S. application filed 02/08/2007 in which claims 31, 41-42, 56-61 are canceled. Claims 1-30, 32-55, 62-91 are presented for examination.

INFORMATION DISCLOSURE STATEMENT

2. The Applicant's submission of the IDS filed 01/23/06, 04/10/06, 01/25/07 have been considered. As required by M.P.E.P. 609 C(2), a copy of the PTOL-1449 is attached to the instant office action.

With regard to IDS filed 07/27/05, the following documents are not considered due to lack of publication date: **a)** IEEE 100, "The authoritative Dictionary of IEEE Standard"; **b)** Nvidia Corporation "GeForce3.Lightspeed Memory Architecture"; **c)** S. Takase and N. Kushiya "DRAM with Flexible Mapping", pp. 348-349 and 506-507 and **d)** Samsung Electronics, "SDRAM Device Operations". See MPEP 609.04(a), CRF 1.98.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 78, 82, 85 are rejected under 35 U.S.C.102 (e) as being anticipated by Roohparvar US. 2005/0073894.

Claim 78, 82, 85, Roohparvar teaches a memory device in an integrated circuit to be coupled to a data bus, comprising:

a first memory portion; a second memory portion [bank of memory (104), fig. 1A];
an interface [memory controller, page 15/[0142] to receive access requests and to forward

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respective access requests to respective ones of said first and second memory portions , said access requests including a first access request causing first data to be read out of or written into said first memory portion and a second access request causing second data to be read out of or written into said second memory portion [REDA/WRITE command, page 6/[0078-0079], said first data being of **one base granularity in size** [Data from any READ burst **may be truncated** with a subsequent READ command, and data from a **fixed-length READ**, page 7/[0087]; Data from any READ burst may be **truncated with a subsequent WRITE command**, page 7/[0088]] said second data being of one base granularity in size and following said first data on said memory bus with substantially **no idle time** [Data from any READ burst may be truncated with a subsequent READ command, and data from a fixed-length READ burst may be immediately followed by data from a subsequent READ command. In either case, **a continuous flow of data** can be maintained, page 7/[0087]; The WRITE may be initiated on the clock edge immediately following the last (or last desired) data element from the READ burst, provided **that I/O contention can be avoided**, page 7/[0088]] in between.

Roohparvar further teaches:

With respect to claim 82, receiving a first access request at said memory device, said first access request for accessing a first memory portion of said memory device and including **a column command** [READ, CAS, fig. 7]; receiving a second access request at said memory device, said second access request for accessing a second memory portion of said memory device and including **a second column command** [CAS, page 7/[0091]; fig. 8].

ALLOWABLE SUBJECT MATTER

4. Claims 1-30, 32-40, 43-55, 91, 62-77, 89-90 are allowed.
5. Claims 79-81, 83-84, 86-88 are objected to as being dependent upon a rejected base claims, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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CONCLUSION

6. Any response to this action should be mailed to:

Under Secretary of Commerce for Intellectual Property and Director of the
United States Patent and Trademark Office

PO Box 1450

Alexandria, VA 22313-1450

or faxed to:

(571) 273-8300, (for Official communications intended for entry)

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PMR) system. Status information for published Applications may be obtained from either Private PMR or Public PMR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pak-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ngoc Dinh whose telephone number is (571) 272-4191.

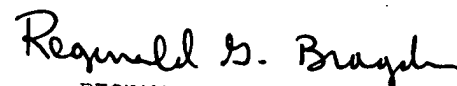
The examiner can normally be reached on Monday-Friday 8:30 AM-5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Reginald Bragdon, can be reached on (571) 272-4204.



NGOC DINH

December 3, 2007



REGINALD BRAGDON
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100